You do not want any instruction that is loaded in memory, to be changed (malware does this)

Inverse is true for data:  
Don’t want data memory to have execute writes, but want to be modifiable.

**Memory address registrer, memory data registers and memory**

**Data:** Ram

Address decoder: Decodes what active line should be activated?  
  
Data: when line powered, goes to memory data register

Memory data register: Actual data stored